

FIG. 1

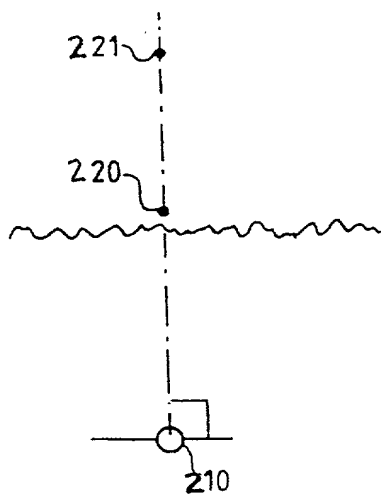


FIG. 2A

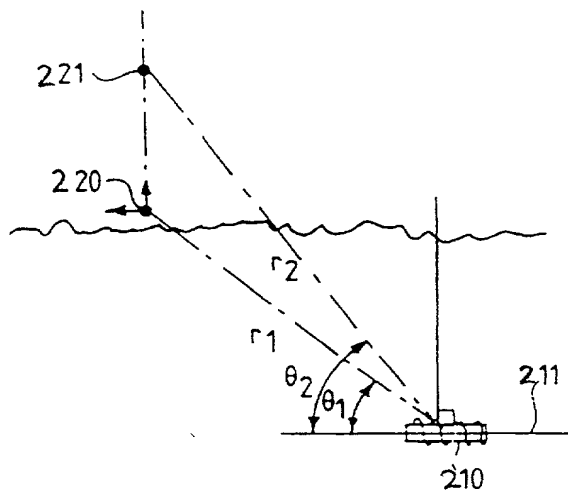


FIG. 2B

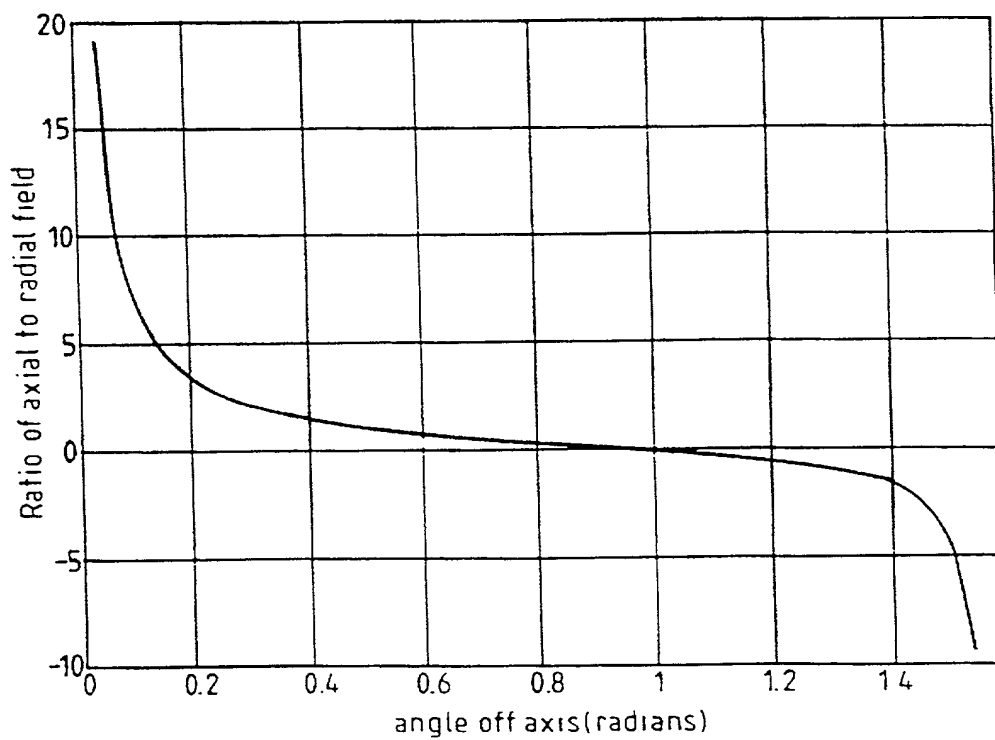


FIG. 3

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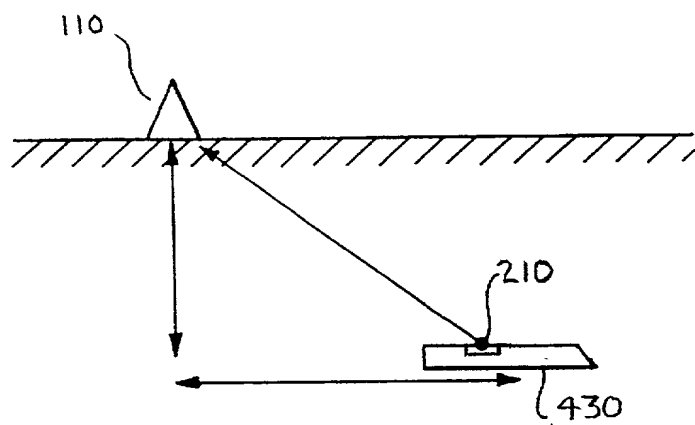


FIG. 4

TRANSMIT DEVICE 102

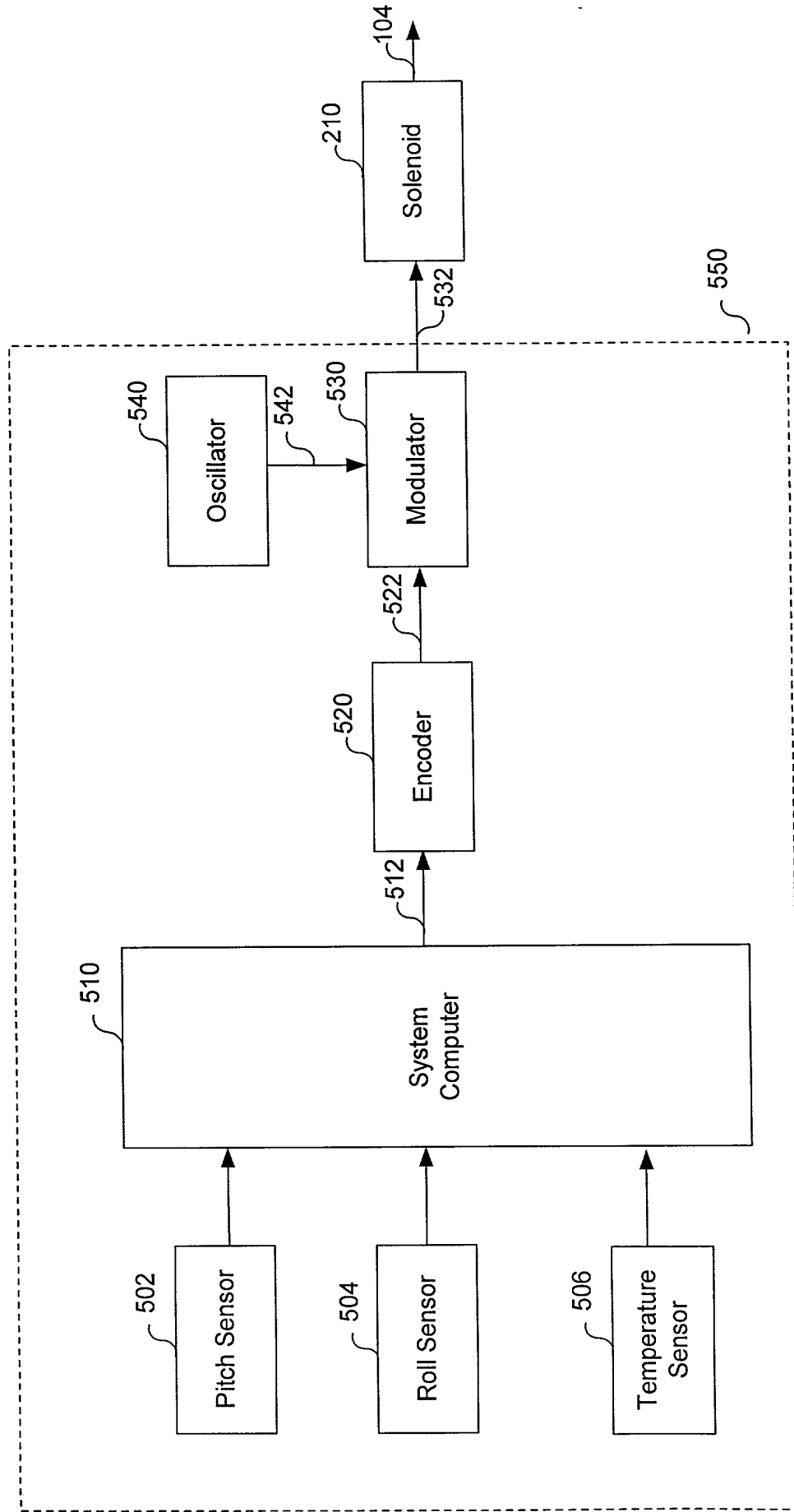


FIG. 5

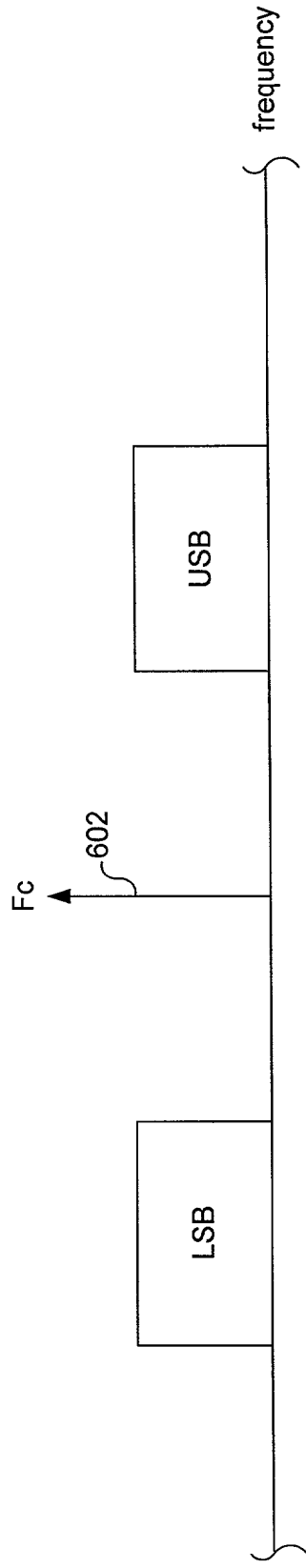


FIG. 6

RECEIVE DEVICE 110

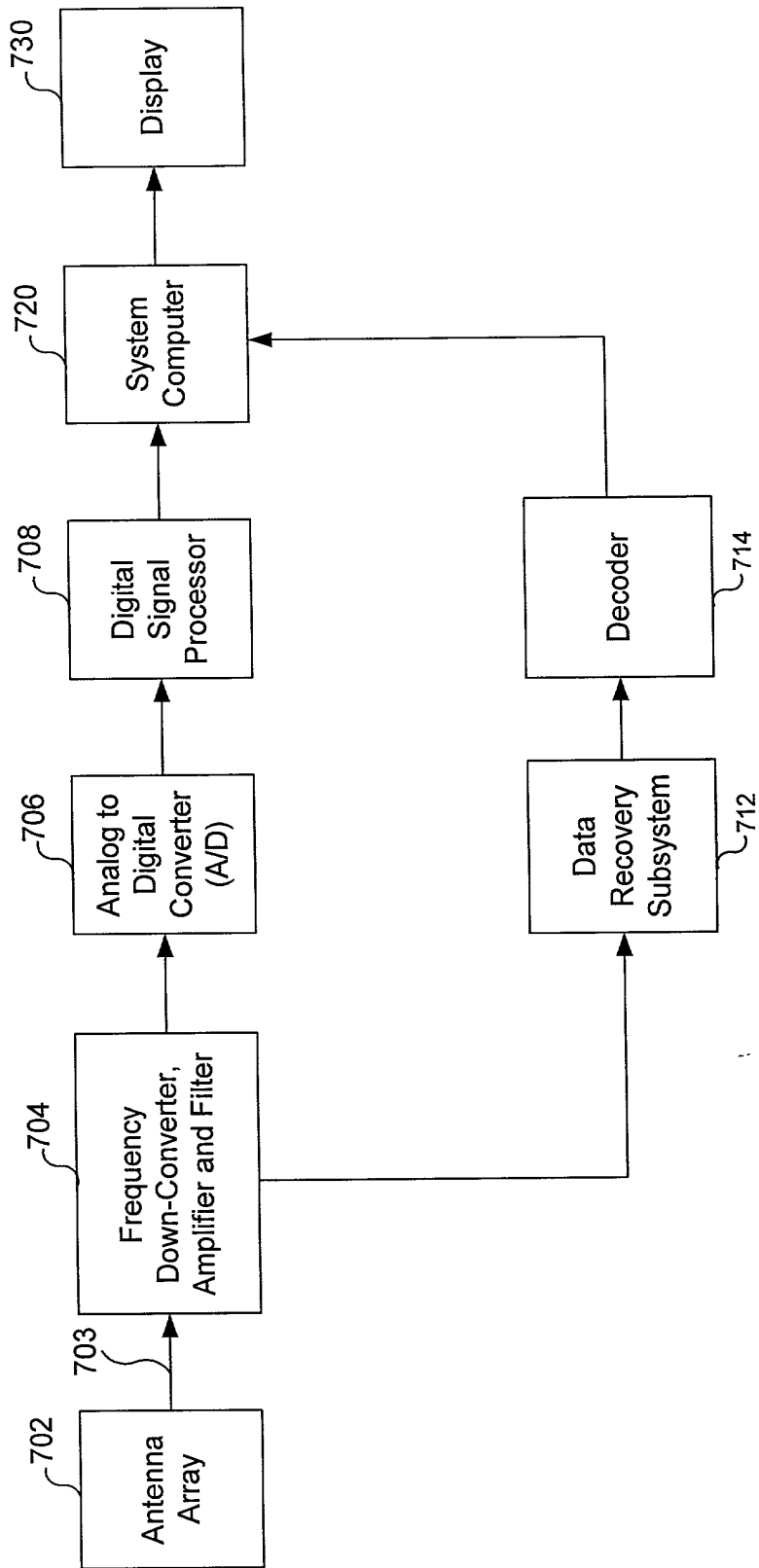


FIG. 7

DIGITAL SIGNAL PROCESSOR 708

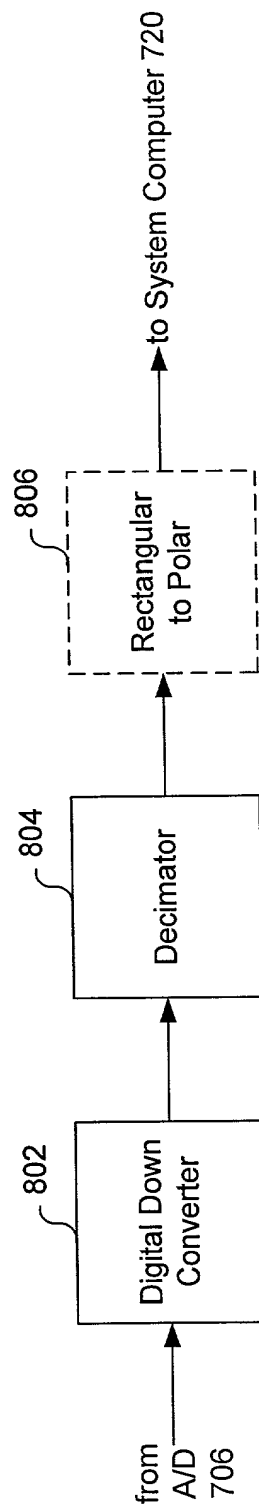


FIG. 8

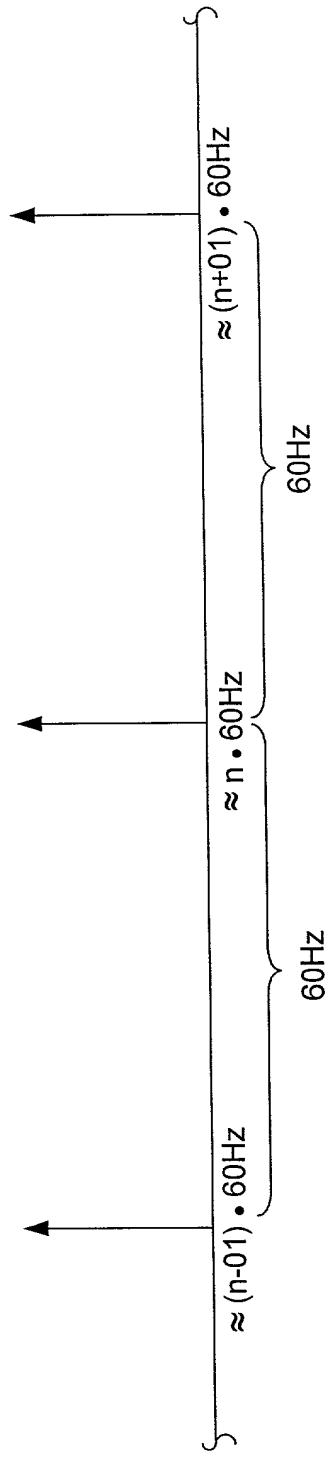


FIG. 9A

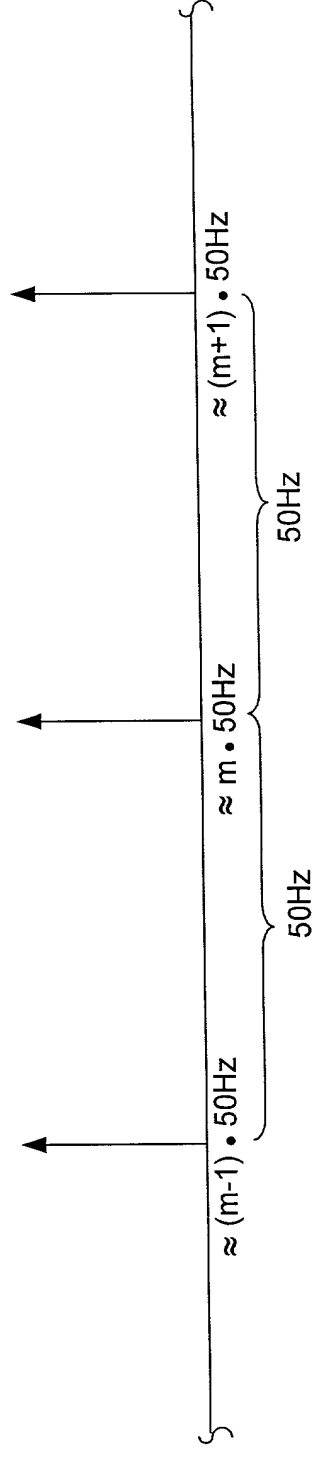


FIG. 9B

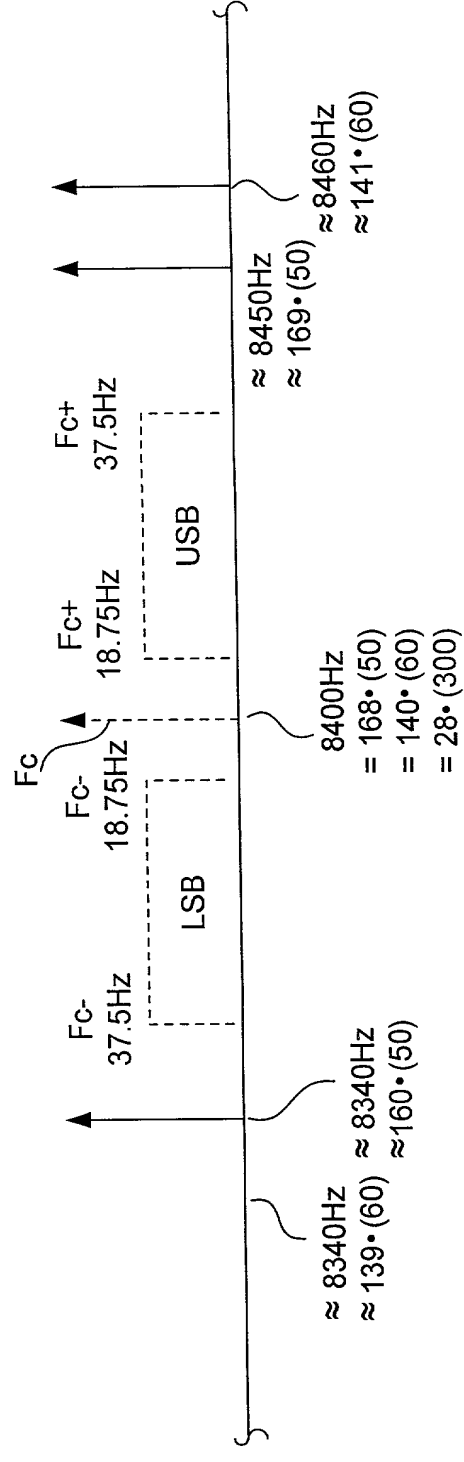


FIG. 9C

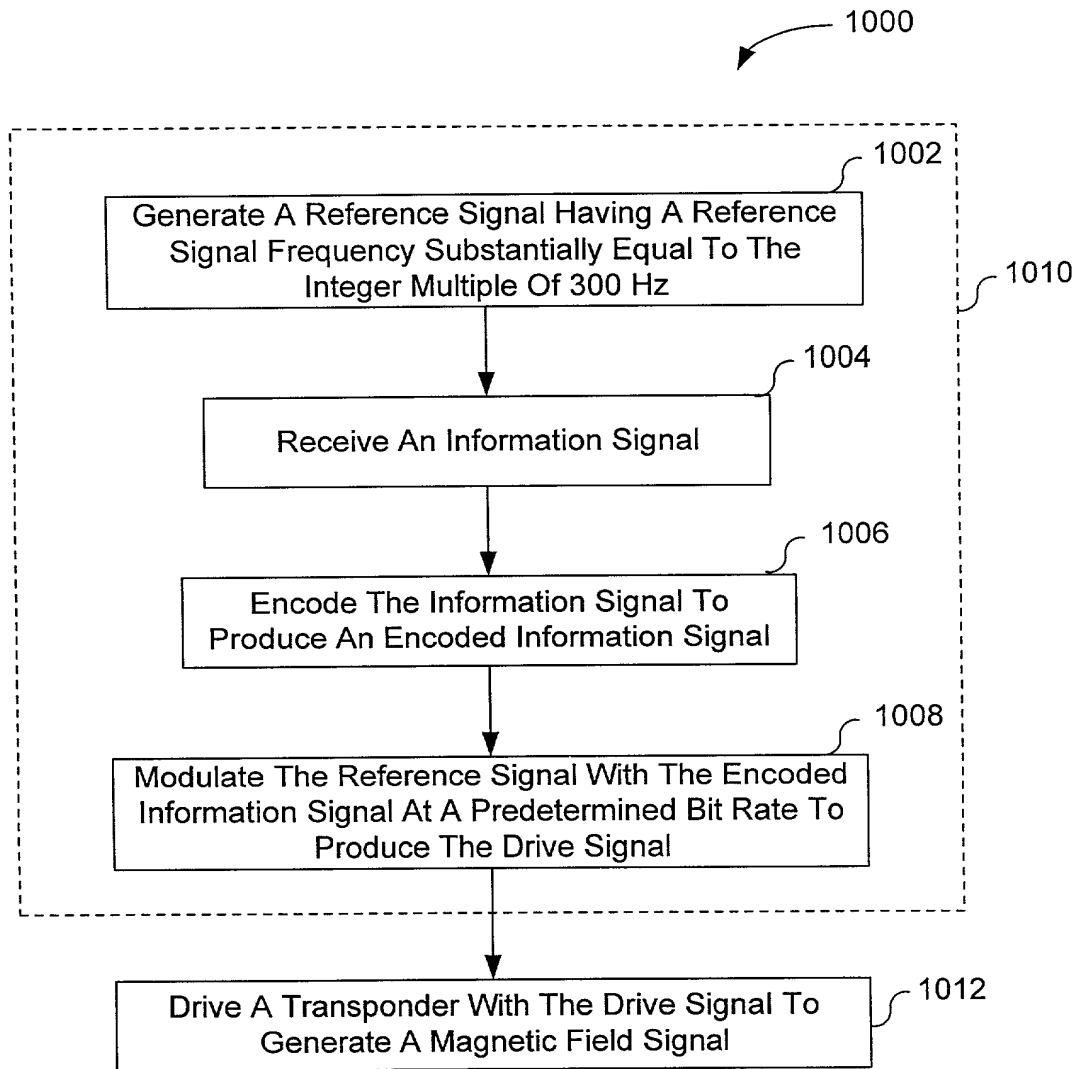
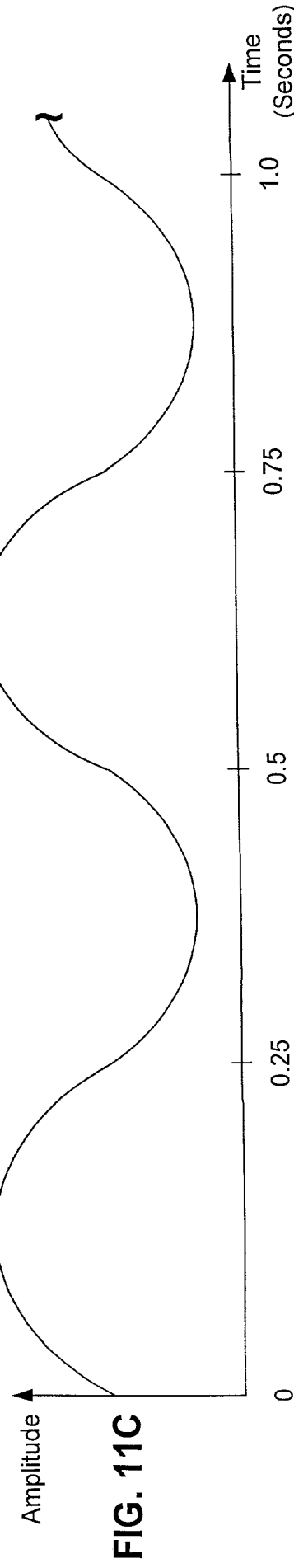
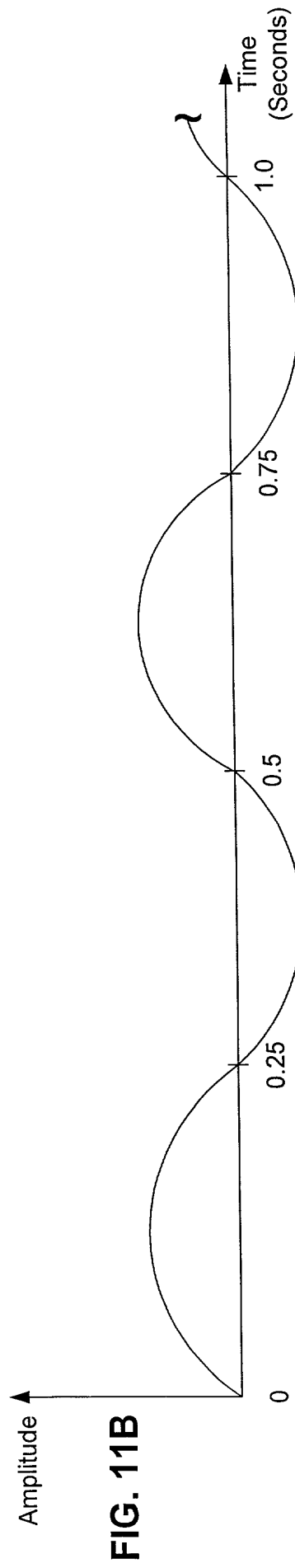
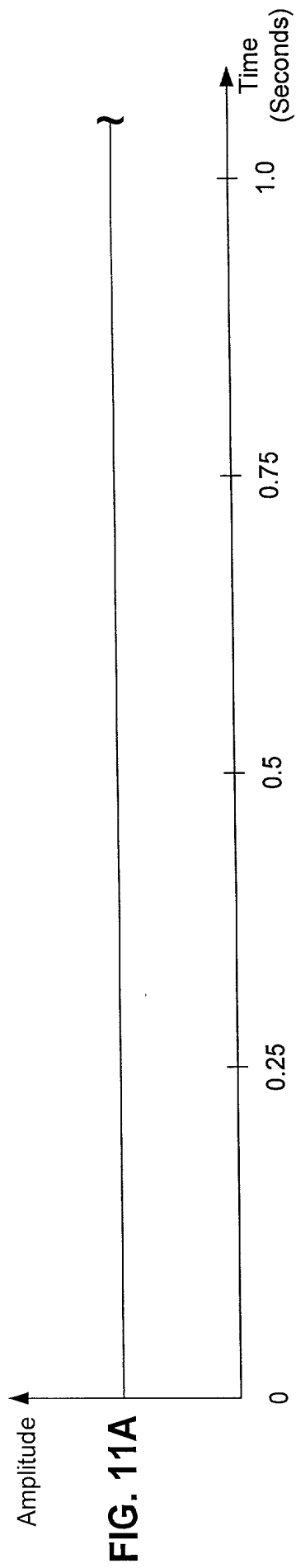


FIG. 10



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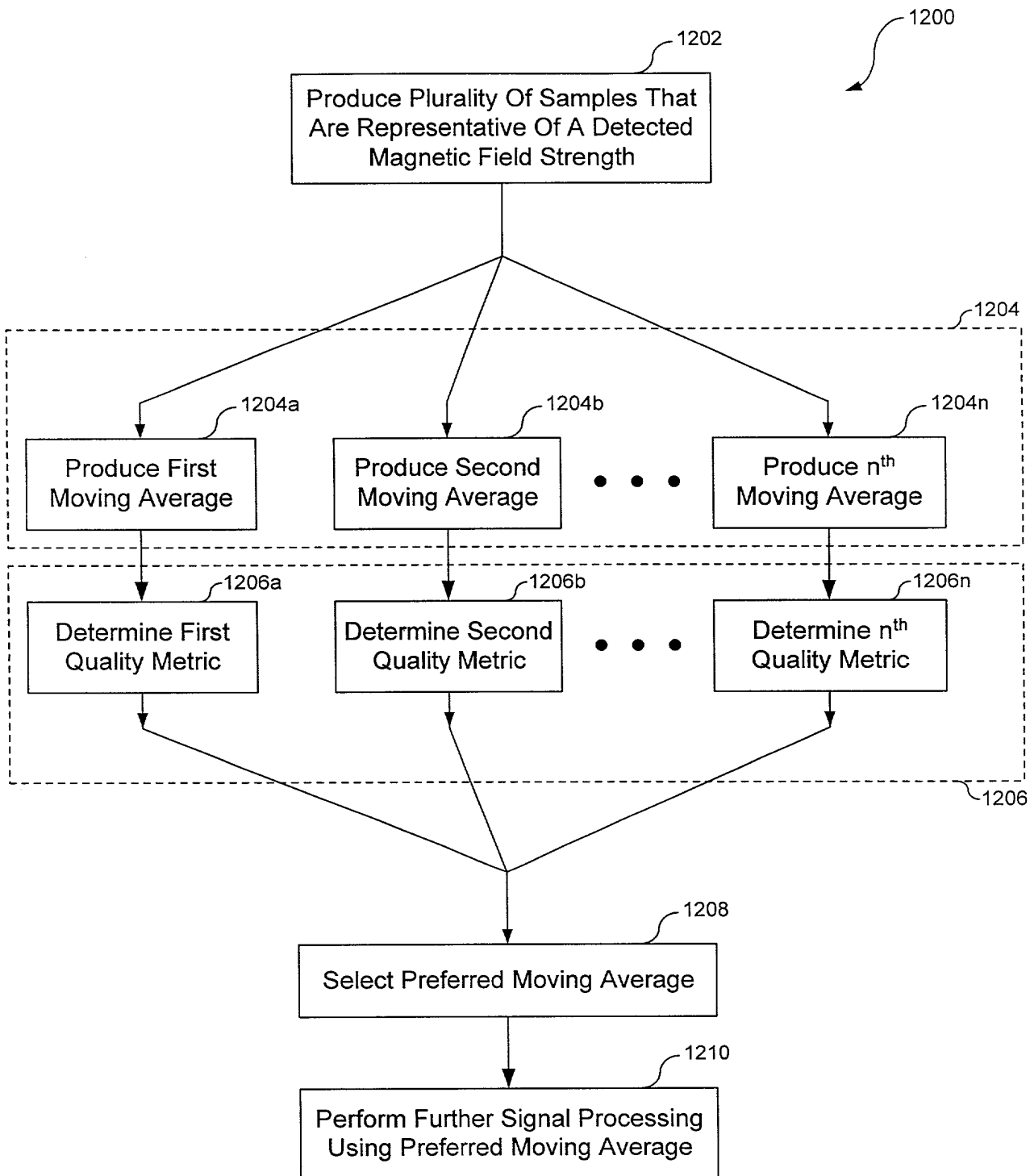


FIG. 12

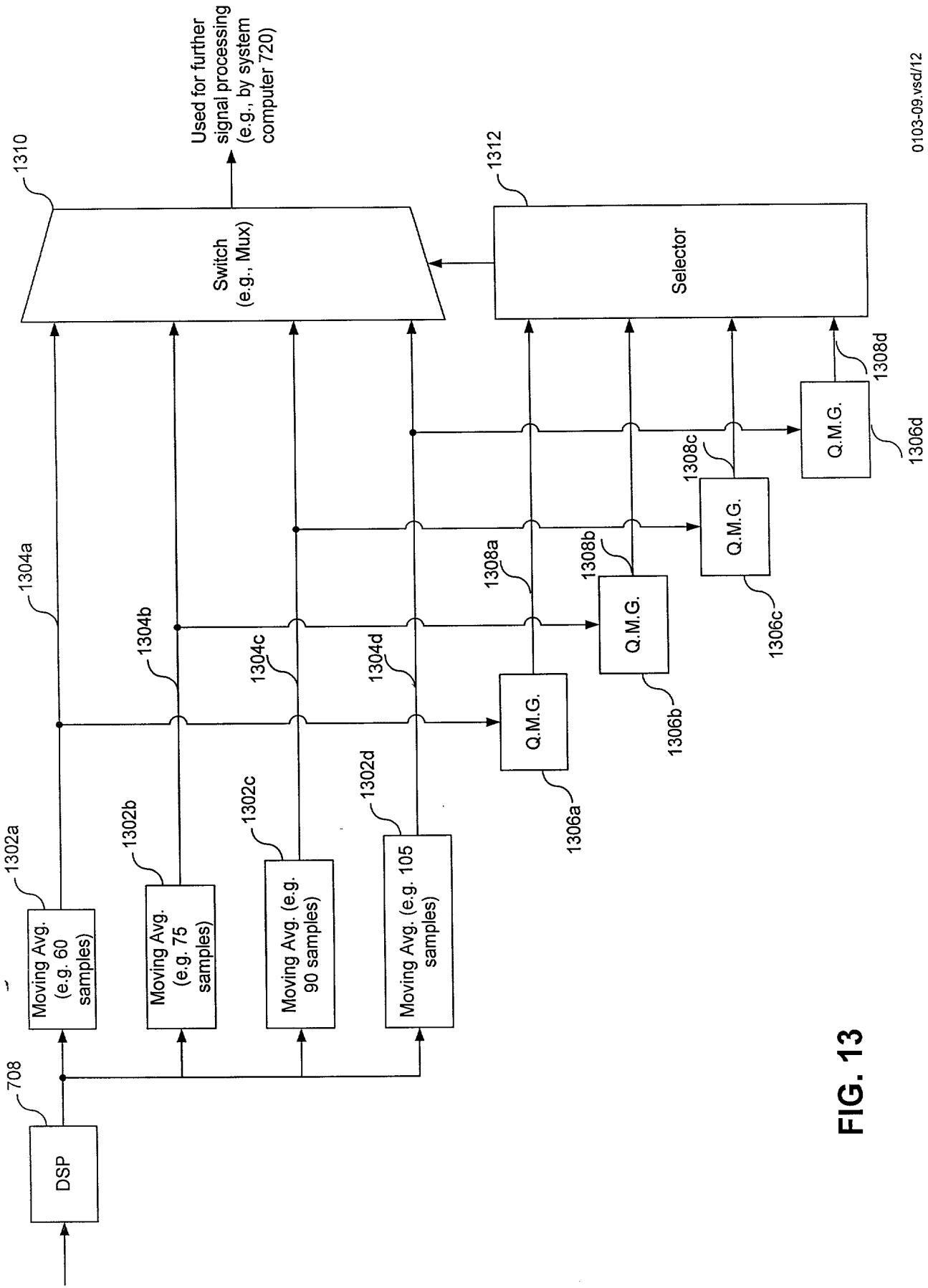


FIG. 13